

Appin

Brent Keeth

Filed

February 11, 1997

For

MEMORY SYSTEM WITH DYNAMIC TIMING

CORRECTION

Docket No.

660073.587

Date

February 11, 1997

Box Patent Application Assistant Commissioner for Patents 2011 Jefferson Davis Highway Washington, DC 20231

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Respectfully submitted,

SEED and BERRY LLP

Jeanette M. West / Brunetta Ishman

CTT:jc

Enclosures:

Postcard

Form PTO-1082 (+ copy)

Specification, Claims, Abstract (16 pages)

Six (6) Sheets of Drawings (Figures 1-6)

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BOX PATENT APPLICATION
ASSISTANT COMMISSIONER FOR PATENTS
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WASHINGTON DC 20231

Sir:

Transmitted herewith for filing is the patent application of:

Inventor:

Brent Keeth

For:

MEMORY SYSTEM WITH DYNAMIC TIMING CORRECTION

Enclosed are:

[X] Six (6) sheets of drawings (Figs. 1-6).

[] An assignment of the invention to: , a corporation of the State of .

[] A Declaration and Power of Attorney.

[] A verified statement to establish small entity status under 37 C.F.R. 1.9 and 37 C.F.R. 1.27.

[] A certified copy of Application No., filed, from which priority is claimed,.

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Total Claims			x 11	\$	or	x 22	\$
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Respectfully submitted, SEED and BERRY LLP

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